REMARKS

Applicants respectfully request favorable reconsideration of this application, as amended. Since this Amendment is deemed fully responsive to the February Office Action, Applicants request that it be considered in lieu of restarting the period for response as previously requested.

Claims 1, 4, 10, and 13 have been amended to include features of claims that depend therefrom and in particular now specify that the parameters for the program unit give positional information for a principal part to be executed at high frequency. Claims 2, 5, 11, and 14 have been cancelled and Claims 3, 6, 12, and 15 have been amended to remove duplicative language that resulted from the above amendments. Claims 16 and 19 have been revised to improve readability.

The Office Action objects to Claims 8 and 9 in that they are in improper form, Claims 1-15 under 35 U.S.C. § 112, second paragraph, as indefinite, and Claims 1-21 under 35 U.S.C. § 102(b) as anticipated by U.S. Patent No. 5,581,737 to Dahlen (hereinafter "Dahlen"). Based on the following remarks, and by virtue of the above amendments,

it will become clear that Dahlen fails to anticipate the claimed invention.

By virtue of the Preliminary Amendment filed October 12, 2000, Applicants submit the claim objections regarding the improper multiple dependencies is moot. The claims have also been generally amended in accordance with the Examiner's recommendations to provide proper antecedent basis. However, regarding the term "any" which now appears in Claims 4, 11, and 14, Applicants submit this term is definite in that it specifies that the entry address differs from an entry address assigned for any part to be executed at high frequency.

As noted above, Claims 1, 4, 10, and 13 recite that the parameters give positional information for a principal part to be executed at a high frequency in the program unit. Claim 1 additionally recites that the principal part of the program unit is assigned an entry address of free cache area. Claims 4, 10 and 13 additionally recite that the entry address to be assigned for the principal part to be executed at a high frequency in the program unit differs from an entry address assigned for any part to be executed at a high frequency in any other program unit. Similarly, Claims 16 and 19 recite that an entry address of a cache

memory to be used for the principal part of the program unit differs from an entry address of the cache memory used for the principal part of one of the plurality of program units other than the program unit.

Dahlen is directed toward a method and apparatus for expansion, contraction and reapportionment of structured external storage structures. However, at no point does Dahlen state that the SES facility 16 has positional information for a principal part of the central processing complexes (CPCs) 10A-10N. Moreover, and as illustrated in marked-up Fig. 3 of Dahlen below, it is clear that the directory 60 contains portions C and X that are shared by both the local cache of system 24A and the local cache of system 24B. Therefore, in Dahlen, the principal part in each of the CPCs is not assigned a different directory space, but rather shared.

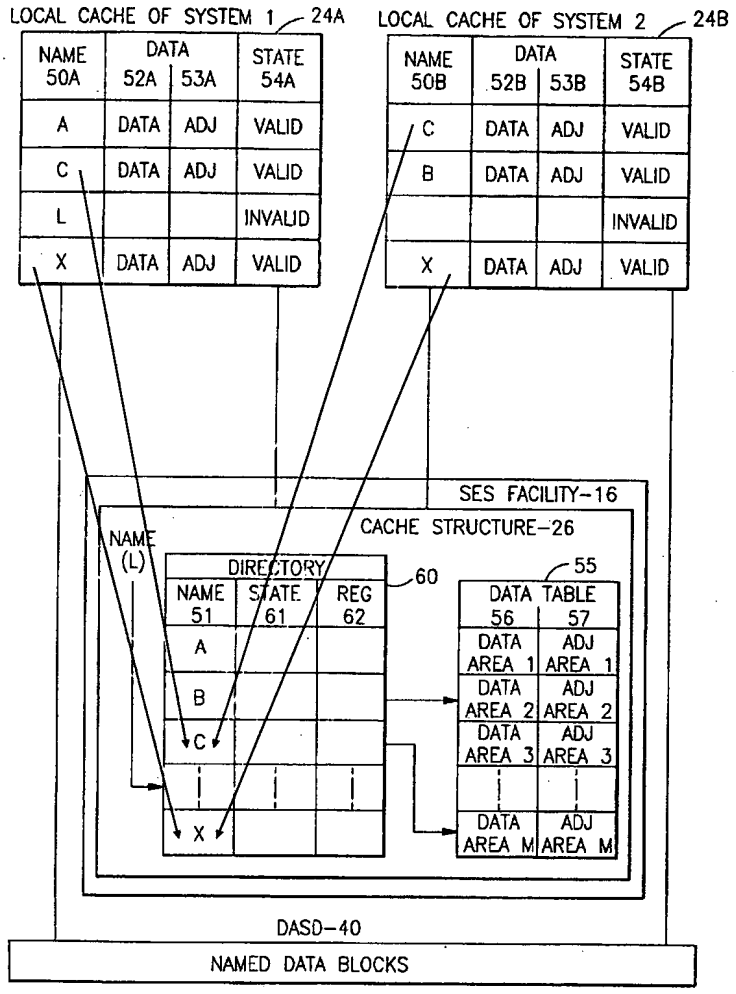

FIG.3

For at least this reason, Dahlen fails to anticipate or suggest the pending independent claims. The claims that depend therefrom depart even further from the prior art,

and should be allowed for at least the same reasons as discussed above.

An early Notice of Allowance is respectfully solicited.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been requested separately, such extension is hereby requested.

Respectfully submitted,

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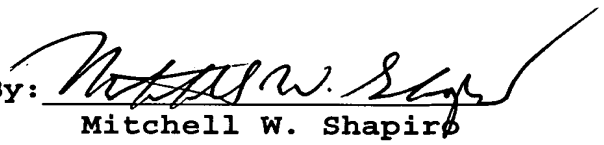
By: 
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